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APPLICATION NO.	FILING DATE	FIR	RST NAMED INVE	NTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/604,731	08/13/2003	Geng Wang			FIS920030209US1	1730
TITLE OF INVENTION: SE	LE-ALIGNED DRAIN/CH	ANNEL JUNCTION	IN VERTICAL	PASS TRANSISTOR	DRAM CELL DESIGN FOR	DEVICE SCALING
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01 FC:1501 1400.0 02 FC:1504 300.0						
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EXAMINER		ART UNIT	C	LASS-SUBCLASS		
DANG, TRUNG Q		2823	<u> </u>	438-268000		
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